UNITED STATES PATENT AND TRADEMARK OFFICE

Jack Hwang et al.

Serial No.:

August 20, 2003

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For:

Filed:

Forming Strained Source Drain

Junction Field Effect Transistors

Art Unit:

2822

Examiner:

Mark V. Prenty

Docket:

ITL.0764D1US

P14416D

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed August 26, 2004, please amend the abovereferenced patent application as follows:

Date of Deposit: September 17, 2004

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 2231,3-1,450.

Cynthia L. Hayden